

REMARKS

Claims 1-81 are pending in this application and have all been allowed. The application, however, was objected to in view of informalities. Applicant has corrected these informalities and now believes that the case is in condition for allowance.

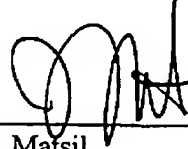
In particular, the specification was objected to because the application number of the co-pending application was blank. In response, the first paragraph of the application has been amended to include the serial number (and filing date) of the co-pending application.

The drawings were objected to because *a first reference voltage line* and *a second reference voltage line* must be shown or the feature(s) canceled from the claim(s). Applicant respectfully submits that the first and second voltage lines were illustrated in the originally filed drawings, in particular in Figure 1 (which was labeled as prior art). To clarify that the capacitors taught by the present invention can be implemented as illustrated in Figure 1, Paragraph [0033] has been amended herein to refer to Figure 1. In particular, Paragraph [0033] now states "The capacitor may be used as a decoupling capacitor in the preferred embodiment Figure 1 shows examples of decoupling capacitors coupled between a first reference voltage line (e.g., OV_{DD}, V_{DD} or GROUND) and a second reference voltage line (e.g., OV_{DD}, V_{DD} or GROUND)." No new matter has been added.

In view of the above, it is respectfully submitted that the present application is in condition for allowance. If for any reason the application is not in condition for allowance,

Applicant invites the Examiner to telephone Applicant's attorney at the below-listed telephone number so that any issues can be resolved.

Respectfully submitted,



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Date

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